

## IBM posts SPEC CPU2006 scores for quad-core x3200 M2

*x3200 M2 achieves leadership SPECint2006 score for a single-socket server using Intel Xeon X3370 processor*

August 12, 2008 ... IBM® System x™ 3200 M2 server is an affordable, single-socket tower server that has been optimized to provide outstanding availability, manageability, and performance features for small to medium-sized businesses, retail stores, or distributed enterprises.

The x3200 M2 systems include features not typically seen in this class of system, such as standard, hardware-based RAID 0/1, 2.5-inch (SFF) hot-swap SAS drives, and redundant power supplies (on select models). The x3200 M2 includes quad- and dual-core Intel® Xeon® processors for applications that require performance and stability; the x3200 M2 also supports Intel Pentium® dual-core and Core 2 Duo processors for applications that require lower cost.

In measurements with the SPEC CPU2006 benchmark suite, the x3200 M2 achieved a leadership SPECint2006 score for a system using the Intel Xeon X3370 processor and competitive scores on the other members of the benchmark suite.

The x3200 M2 was configured with the Quad-Core Intel Xeon Processor X3370 (3.00GHz, 12MB L2 cache, and 1333 MHz front-side bus—1 processor/4 cores/4 threads) and 8GB of DDR2 PC2-6400 memory, and ran SUSE Linux® Enterprise Server 10 SP1 x64. (1)

The scores in the following tables are the first SPEC CPU2006 results published for this processor model.

<b>SPEC CPU2006 Benchmark</b>	<b>x3200 M2 – Quad-Core Intel Xeon Processor X3370 (3.00GHz, 12MB L2 Cache, 1333 MHz FSB)</b>
SPECint2006	26.3
SPECint_rate2006	76.2
SPECint_rate_base2006	66.2
SPECfp2006	24.2
SPECfp_rate2006	51.8
SPECfp_rate_base2006	47.8

Results are current as of August 12, 2008. The scores have been submitted to SPEC for review and will be posted on their Web site upon successful completion of the review. View all published results at [www.spec.org](http://www.spec.org).

(1) The x3200 M2 model using the Quad-Core Intel Xeon Processor X3370 (3.00GHz, 12MB L2 cache, and 1333 MHz FSB) is planned to be generally available September 19, 2008.

IBM and System x are trademarks or registered trademarks of International Business Machines Corporation.

Intel, Xeon and Pentium are trademarks or registered trademarks of Intel Corporation.

Linux is a registered trademark of Linus Torvalds in the United States, other countries, or both.

SPEC, SPECfp, and SPECint are registered trademarks of the Standard Performance Evaluation Corporation.

All other company/product names and service marks may be trademarks or registered trademarks of their respective companies.