IBM posts SPEC CPU2006 scores for x3400 M3

x3400 M3 delivers competitive processor performance for compute-intensive applications

March 16, 2010 ... IBM® today announces SPEC® CPU2006 benchmark scores for the IBM System x®3400 M3 server using the new Intel® Xeon® Processor 5600 series.

Configured with the Intel Xeon Processor X5670, the x3400 M3 delivered competitive scores on the SPEC CPU2006 benchmark suite. The x3400 M3 was configured with two Intel Xeon X5670 processors (2.93GHz, 256KB L2 cache per core and 12MB L3 cache per processor—2 processors/12 cores/24 threads), 48GB of DDR3 PC3-10600R memory, and SUSE Linux® Enterprise Server 11 x64. (1)

Configured with the Intel Xeon Processor E5640, the x3400 M3 delivered competitive scores on the SPEC CPU2006 benchmark suite. The x3400 M3 was configured with two Intel Xeon E5640 processors (2.66GHz, 256KB L2 cache per core and 12MB L3 cache per processor—2 processors/8 cores/16 threads), 48GB of DDR3 PC3-10600R memory, and SUSE Linux Enterprise Server 11 x64. (2)

The scores in the following table are the first SPEC CPU2006 results published for these x3400 M3 processor models.

SPEC CPU2006 Benchmark	IBM System x3400 M3 Six-Core Intel Xeon Processor X5670 – 2.93GHz	IBM System x3400 M3 Quad-Core Intel Xeon Processor E5640 – 2.66GHz
SPECint®2006	39.4	34.4
SPECint_base2006	36.6	32.0
SPECint_rate2006	349	239
SPECint_rate_base2006	326	225
SPECfp®2006	45.6	39.3
SPECfp_base2006	42.6	36.9
SPECfp_rate2006	241	176
SPECfp_rate_base2006	234	169

The System x3400 M3 servers are self-contained, high-performance, 5U tower systems designed for Web and business server applications in remote or distributed environments. These servers are highly scalable in configuration, performance, and availability. These flexible tower models deliver the best blend of power, manageability, expandability, and serviceability. They meet the requirements of server applications in the small-to-mainstream businesses that need an affordable general-purpose network server. These servers fit into business environments where tower configurations are required.

Results are current as of March 16, 2010. The scores have been submitted to SPEC for review and will be posted on their Web site upon successful completion of the review. View all published results at www.spec.org.

(1) The x3400 M3 model using the Quad-Core Intel Xeon X5670 processor is planned to be generally available March 31, 2010. The x3400 M3 as configured for this benchmark will be available March 31, 2010.

(2) The x3400 M3 model using the Quad-Core Intel Xeon E5640 processor is planned to be generally available March 31, 2010. The x3400 M3 as configured for this benchmark will be available March 31, 2010.

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