Lenovo



# Introducing the Balanced Profile UEFI setting for Intel Optane Persistent Memory Planning / Implementation

The Intel Optane Persistent Memory (PMem or DCPMM) provides higher memory capacity support than traditional DRAM which can benefit various applications with massive memory requirements such as virtualization, high-performance computing (HPC), big-data analytics, in-memory databases, Video on Demand (VoD) and SAP HANA.

PMem can be configured in Memory Mode, App Direct Mode, and Mixed Mode (combine Memory and App Direct Mode) to adapt to different user scenarios. Focusing on Memory Mode, this paper elaborates on the low-level access mechanism between DRAM and PMem to explain the difference between the Balanced, Bandwidth Optimized and Latency Optimized profiles for PMem.

**Note**: The Balanced Profile setting is currently only available on systems with Intel Optane Persistent Memory 100 Series and second-generation Intel Xeon Scalable processors.

#### **Two-Level Memory Architecture**

Managed by the memory controller inside Intel Xeon Scalable Processor, PMem adopts a Two-Level Memory (2LM) Architecture in Memory Mode as shown in the following figure. Different from a legacy memory hierarchy, the DRAM under the 2LM topology is treated as an L4 cache to store frequently used data, alongside the PMem working as system memory to provide a larger memory capacity compared to DRAM alone for memory hungry applications.

In 2LM mode, DRAM acts as the next level cache (L4), and PMem acts as system main memory. When data is found in DRAM, it is an L4 hit and access latency is the same as the latency to DRAM. However, when data is not found at DRAM, it is an L4 miss, and memory controller has to access data from PMem. This access from PMem, when it happens, incurs higher memory latency.



Figure 1. 2LM Architecture

### **UEFI Preset Modes for PMem**

Based on the 2LM topology, the memory controller can only request data from either DRAM or the PMem at a time, such that the dispatch ratio between DRAM and PMem would be a crucial factor of the system performance. Fine tuning numerous thresholds along with dispatch ratio of memory controller and related parameters in the BIOS, Lenovo introduces a new Balanced Profile for PMem memory mode. Based on internal test results, the performance of both memory bandwidth and latency improved significantly when the Balanced Profile was applied.

With the introduction of Balanced Profile, Lenovo now offer three different PMem preset modes in the BIOS to adapt various kind of workloads, as listed in the following table.

Profile	Purpose
Balanced (default)	Optimize PMem Profile for Memory Mode
BW Optimized	Optimize Bandwidth on both L4 cache and PMem
Latency	Optimize L4 cache Latency in the presence of PMem Bandwidth

Table 1. PMem preset modes

While the Balanced Profile is recommended as the default preset mode for PMem memory mode, users can change preset mode as needed by clicking through the following menu selections in System Setup in UEFI: System Settings > Socket Configuration > Memory Configuration > NGN Configuration > NVM Performance Setting. Then select from these choices as shown in the following figure:

- BW Optimized
- · Latency Optimized
- Balanced Profile



Figure 2. PMem performance mode settings (click the image for a larger version)

OneCLI V2.5.0 or later supports Intel PMem and can also be used to setup the PMem performance mode by using the OneCLI command as shown below:

```
./OneCli config set IntelOptaneDCPMM.DCPMMPerformanceSetting "Latency Optimized"
./OneCli config set IntelOptaneDCPMM.DCPMMPerformanceSetting "BW Optimized"
./OneCli config set IntelOptaneDCPMM.DCPMMPerformanceSetting "Balanced Profile"
```

Example out of the OneCLI config set command is shown in the following figure.

```
[root@aep lnvgy-utl-lxce-onecli]# ./OneCli config set IntelOptaneDCPMM.
DCPMMPerformanceSetting "Latency Optimized"
Lenovo XClarity Essentials OneCLI lxce_onecli01o-3.0.0
(C) Lenovo 2013-2020 All Rights Reserved
OneCLI License Agreement and OneCLI Legal Information can be found at t
he following location:
    "/opt/lenovo/lnvgy-utl-lxce-onecli/Lic"
Invoking SET command ...
Connected to the node(1) by KCS
IntelOptaneDCPMM.DCPMMPerformanceSetting=Latency Optimized
Configure successfully, please reboot system.
Succeed.
```

Figure 3. OneCLI config set command (click the image for a larger version)

#### About the author

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#### **Related product families**

Product families related to this document are the following:

• Memory

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