



Lenovo Configuration Guide for Confidential Computing and Microsoft CVM

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**Includes detailed steps for
configuring Confidential
Computing for AMD-Based
Systems**

**Provides all details for all required
UEFI settings**

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Abstract

Confidential Computing is a groundbreaking security technology that protects data in use by performing computations in a hardware-based, isolated Trusted Execution Environment (TEE). While traditional security measures protect data at rest (in storage) and data in transit (across networks), data remains vulnerable when it is actively processed in memory. As organizations increasingly migrate sensitive workloads to multi-tenant cloud environments and embrace collaborative AI, securing data during runtime has become a critical business imperative.

This white paper explains how to configure Lenovo ThinkAgile MX server firmware settings to support Microsoft’s implementation of Confidential Virtual Machines (CVM). At the time of this writing, CVM only supports AMD platforms. We use the ThinkAgile MX455 V3 server for examples throughout this paper.

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Introduction

Confidential Computing is a groundbreaking security technology that protects data in use by performing computations in a hardware-based, isolated Trusted Execution Environment (TEE). While traditional security measures protect data at rest (in storage) and data in transit (across networks), data remains vulnerable when it is actively processed in memory. As organizations increasingly migrate sensitive workloads to multi-tenant cloud environments and embrace collaborative AI, securing data during runtime has become a critical business imperative.

This introduction explores how Confidential Computing fills the final gap in data encryption, enabling zero-trust cloud computing, secure multi-party collaboration, and robust regulatory compliance.

The Runtime Security Gap

Traditional enterprise security frameworks successfully protect data in two of its three primary states. Data at rest is secured via storage encryption, and data in transit is protected using cryptographic network protocols like TLS.

However, a critical vulnerability remains; data in use. To process data, a CPU must decrypt it into system memory (RAM). During execution this plain text data is exposed to:

- ▶ Unprivileged system administrators.
- ▶ Malicious insiders with physical or root access.
- ▶ Compromised host operating systems or hypervisors.
- ▶ Advanced memory-dumping malware and side-channel exploits.

Confidential Computing addresses this fundamental flaw. It extends encryption into the processor itself, completing the lifecycle of end-to-end data protection.

Hardware-Based Isolation: The TEE

The core mechanism of Confidential Computing is the Trusted Execution Environment (TEE), often referred to as a hardware enclave. TEEs are isolated memory regions enforced directly at the silicon level by modern CPUs (e.g., AMD SEV-SNP)

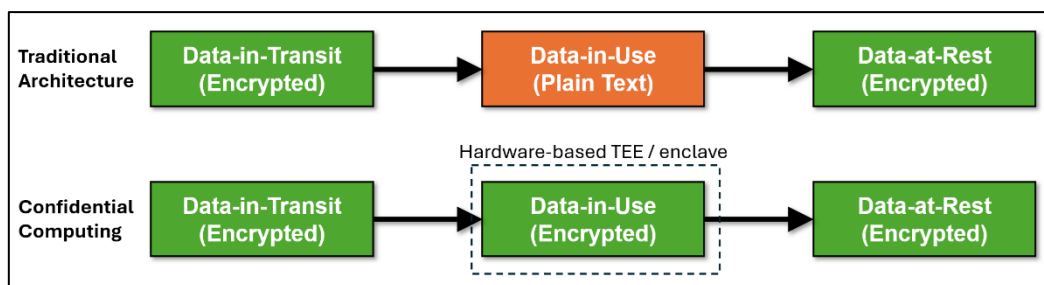


Figure 1: Confidential computing vs. traditional architecture

When an application runs inside a TEE:

- ▶ Memory Encryption: The CPU transparently encrypts the enclave's memory pages using hardware-managed keys.
- ▶ Access Control: Any access attempt by the host operating system, hypervisor, or peer virtual machines is blocked at the hardware level.
- ▶ Cryptographic Attestation: The CPU generates a signed hardware report proving that the exact, untampered application code was loaded securely inside a genuine TEE.

Engineering Drivers for Implementation

For infrastructure, DevOps, and security engineers, implementing Confidential Computing satisfies three non-negotiable operational requirements:

- ▶ **True Zero-Trust Architecture:** Engineers can treat the underlying cloud provider, host infrastructure, and virtualization layers as completely untrusted. You own the data keys; the platform provider cannot inspect your code or state.
- ▶ **Secure Multi-Party Computation (SMPC):** It enables data sharing and collaborative analytics (such as joint machine learning training) with external partners without ever exposing raw datasets to the other parties.
- ▶ **De-risking Cloud Migration:** High-risk workloads—such as cryptographic key management, financial transaction processing, and proprietary AI models—can be safely lifted and shifted to public, multi-tenant cloud environments.

Confidential Computing is supported by Microsoft as “Confidential VMs” (CVM), which provide strong isolation boundaries by extending data protection from storage and networks directly into the hardware. CVMs use hardware-based Trusted Execution Environments (TEEs) to make a virtual machine's runtime memory completely opaque to the underlying infrastructure.

For additional information regarding Confidential Computing and Confidential VMs from Microsoft, refer to the following article:

<https://azure.microsoft.com/en-us/solutions/confidential-compute>

Configure UEFI settings

To enable SEV-SNP the following UEFI settings must be configured:

AMD Secure Memory Encryption (SME)

This setting must be set to Enabled.

Path: UEFI Settings → System Settings → Memory → SMEE → Enabled

Description: This setting is required to use all Secure Encrypted Virtualization features and uses a single key to encrypt system memory and provides additional security benefits. For example, when enabled it can protect the Hypervisor and the Host OS.

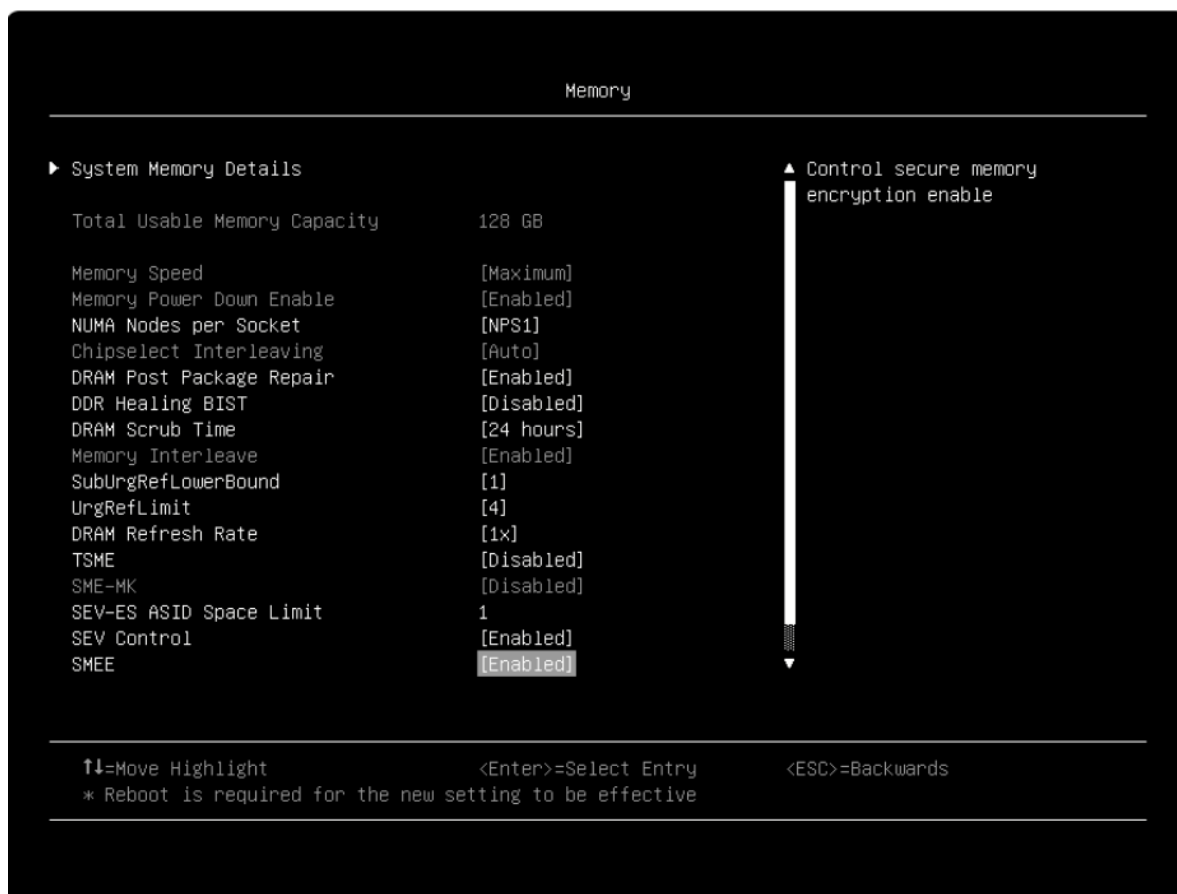


Figure 2: System Setup screen showing the SMEE setting

Transparent Secure Memory Encryption (TSME)

This setting must be set to Enabled.

Path: UEFI Settings → System Settings → Memory → TSME → Enabled

Description: When enabled, it provides full memory encryption without requiring changes to the operating system or applications. It is a simplified version of AMD Secure Memory Encryption. TSME is required for DRTM.

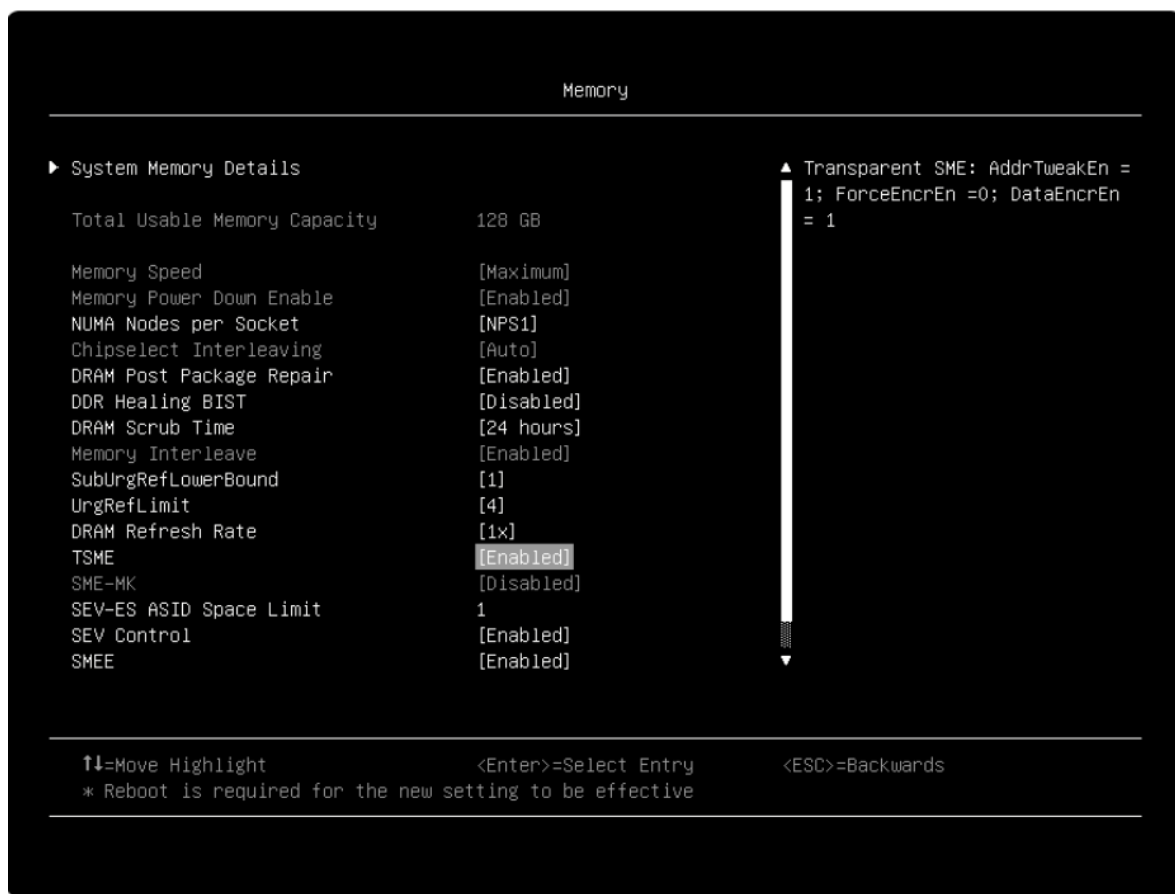


Figure 3: System Setup screen showing the TSME setting

Secure Encrypted Virtualization Address Space Identifier (ASID) Space Limit

Set this value to "ASID Count + 1"

SEV-ES virtual machines are the most secure virtual machines.

Path: UEFI Settings → System Settings → Memory → SEV-ES ASID Space Limit → value

Description: SEV VMs using ASIDs below the SEV-ES ASID Space Limit must enable the SEV-ES feature. ASIDs from the SEV-ES ASID Space Limit to (SEV ASID Count + 1) can only be used with SEV VMs. If this field is set to (SEV ASID Count + 1), all ASIDs are forced to be SEV-ES ASIDs.



Figure 4: System Setup screen showing the SEV-ES ASID Space Limit setting

Secure Encrypted Virtualization Control

This setting must be set to Enabled.

Path: UEFI Settings → System Settings → Memory → SEV Control → Enabled

Description: This setting enables SEV control. Enables confidential computing by protecting VM memory and state from unauthorized access.

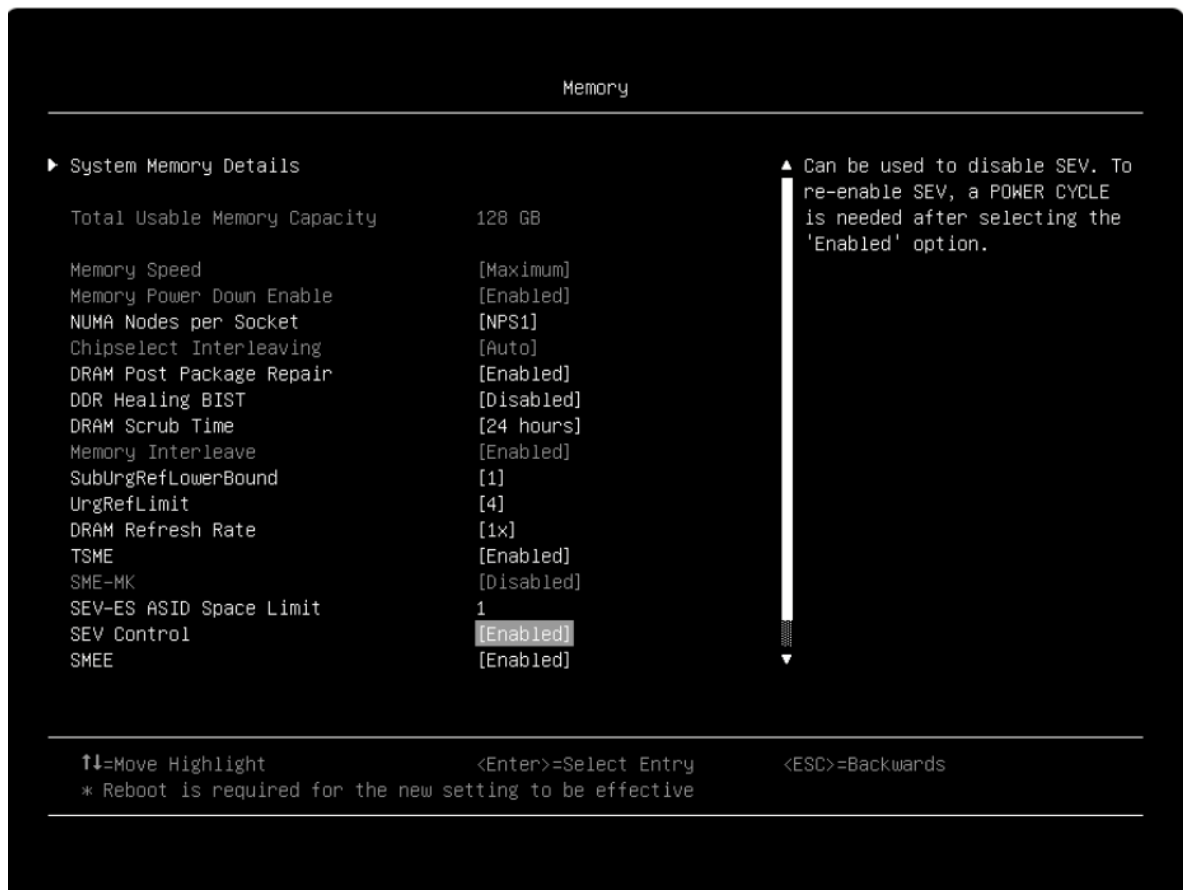


Figure 5: System Setup screen showing the SEV Control setting

SVM Mode

This setting must be set to Enabled.

Path: UEFI Settings → System Settings → Processors → SVM Mode → Enabled

Description: This setting enables CPU Virtualization. Enables Secure Virtual Machine Mode and is required for SEV-ES and SEV-SNP and for Confidential Computing features.

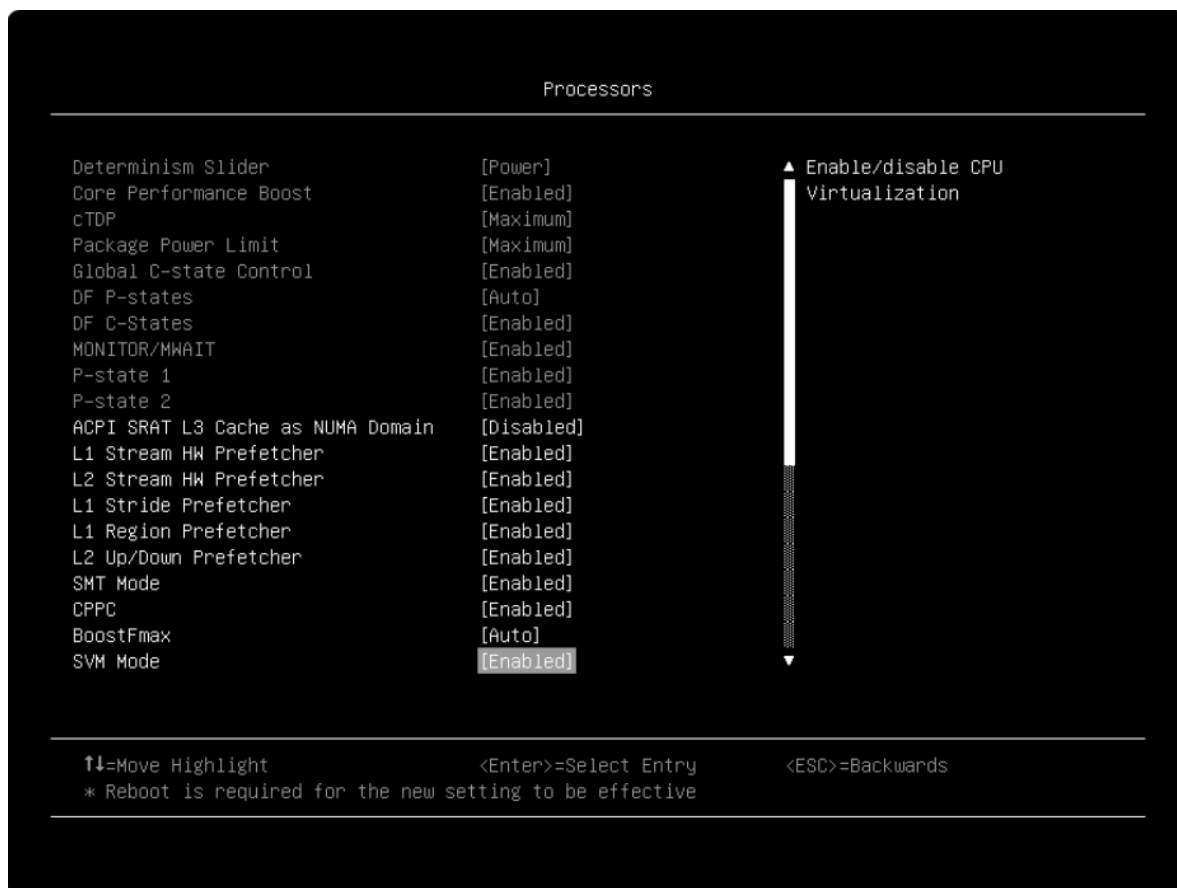


Figure 6: System Setup screen showing the SVM Mode setting

Secure Encrypted Virtualization-Secure Nested Paging (SNP)

This setting must be set to Enabled.

Path: UEFI Settings → System Settings → Processors → SEV-SNP Support → Enabled

Description: This setting enables SEV-SNP support. Provides memory integrity protection and guest isolation from the hypervisor.

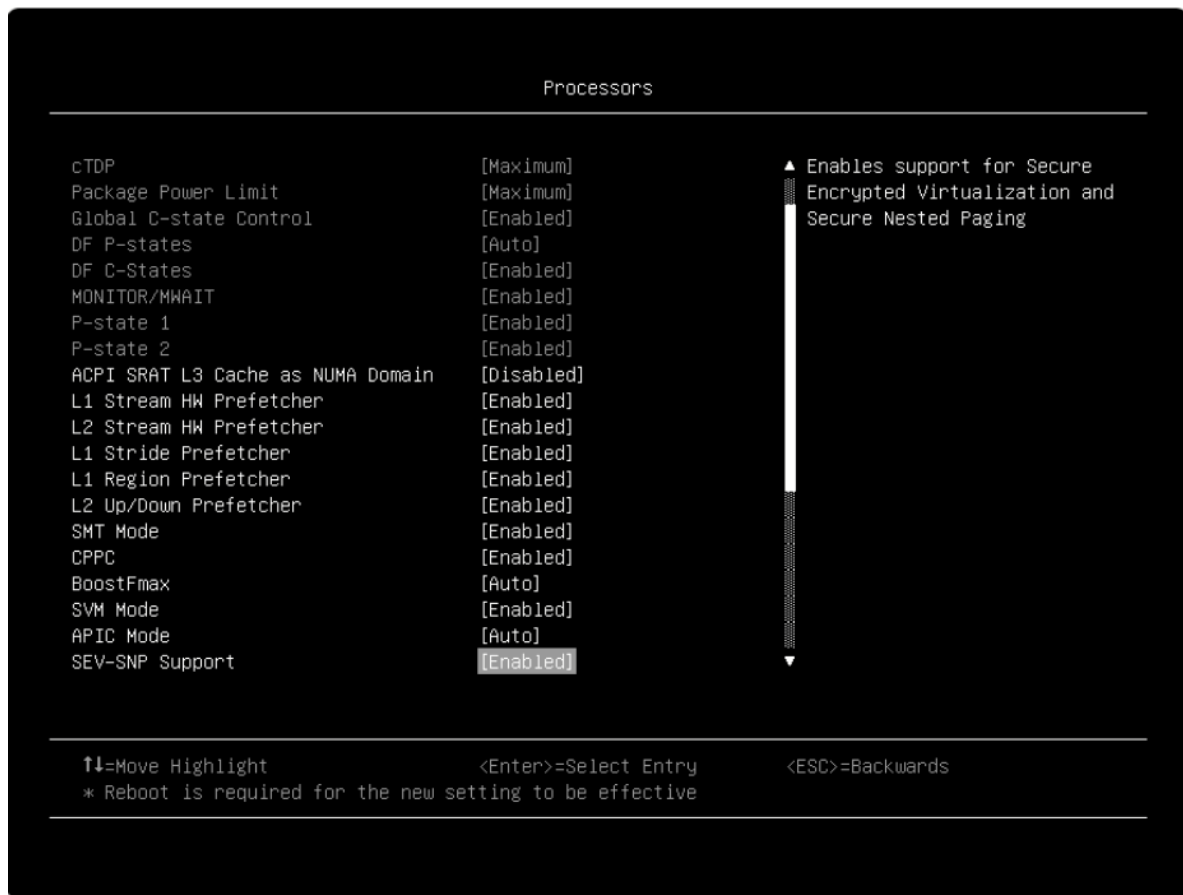


Figure 7: System Setup screen showing the Secure Encrypted Virtualization SNP Support setting

Secure Nested Paging (SNP) RMP Table Coverage

This setting must be set to Disabled.

Path: UEFI Settings → System Settings → Processors → SNP Memory (RMP Table Coverage) → Disabled

Description: The use of the Reverse Map Table (RMP) Table to protect memory when SEV-SNP is enabled. This is not applicable to Hyper-V as it allocates and manages its own RMP table instead of platform firmware

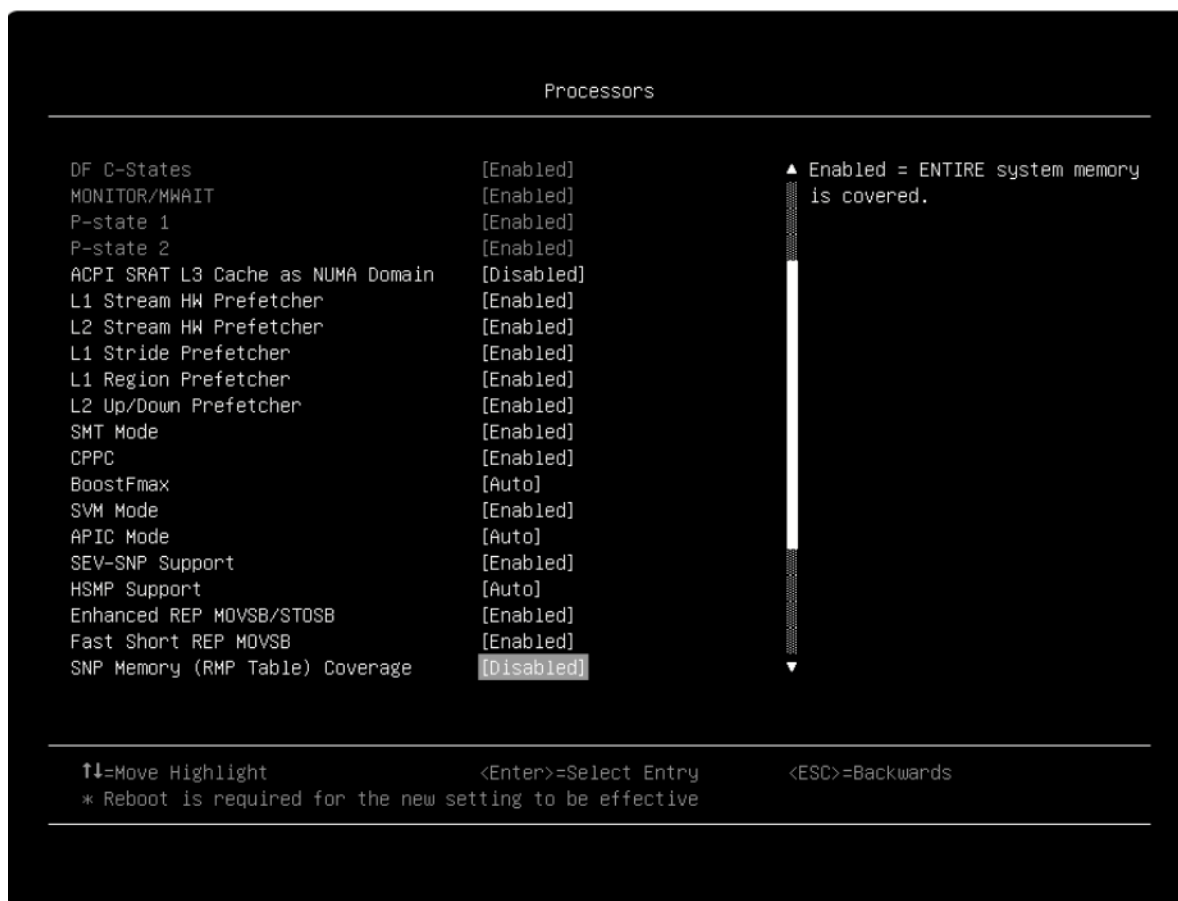


Figure 8: System Setup screen showing the Secure Nested Paging Coverage setting

IOMMU

This setting must be set to Enabled.

Path: UEFI Settings → System Settings → Processors → Secured Core → IOMMU → Enabled

Description: This setting enables Input-Output Memory Management Unit (IOMMU) support. Enables the Input-Output Memory Management Unit and is required for SEV-SNP enablement. The IOMMU is an AMD EPYC feature that manages and translates memory addresses between devices and system memory.



Figure 9: System Setup screen showing the IOMMU setting

UEFI settings are now configured properly to support Confidential Computing.

Change history

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- ▶ Garrett Tashiro, Microsoft Cloud Solution Engineer
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